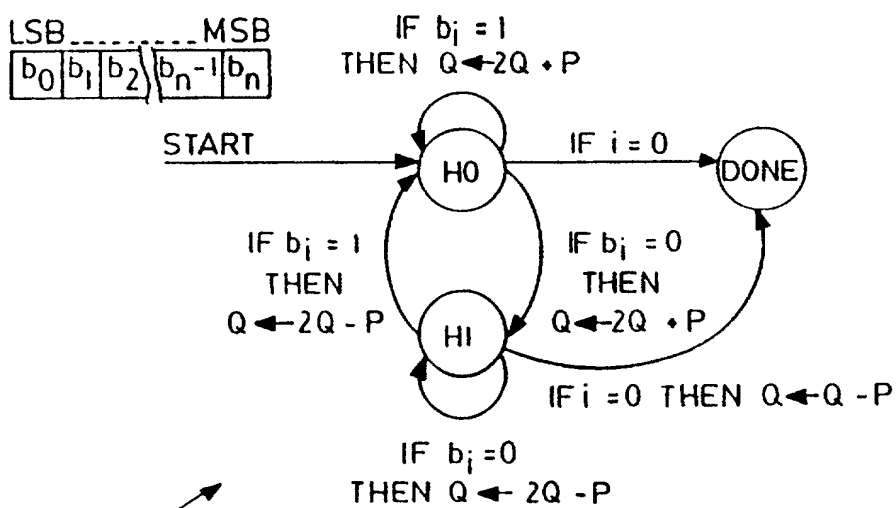


FIG. 1

$$\begin{aligned}
 629 &= \begin{array}{c} 2^9 \quad 2^8 \quad 2^7 \quad 2^6 \quad 2^5 \quad 2^4 \quad 2^2 \quad 2^0 \\ \begin{array}{|c|c|c|c|c|c|c|c|} \hline 1 & 0 & 0 & 1 & 1 & 1 & 0 & 1 \\ \hline +1 & +1 & -1 & -1 & +1 & +1 & +1 & -1 \\ \hline \end{array} \end{array} \\
 &= 2^9 + (2^8 - 2^7 - 2^6) + 2^5 + 2^4 + (2^3 - 2^2) + (2^1 - 2^0) \\
 628 &= \begin{array}{c} 2^9 \quad 2^6 \quad 2^5 \quad 2^4 \quad 2^2 \\ \begin{array}{|c|c|c|c|c|} \hline 1 & 0 & 0 & 1 & 1 \\ \hline +1 & +1 & -1 & -1 & +1 \\ \hline \end{array} \end{array} \\
 &= 2^9 + (2^8 - 2^7 - 2^6) + 2^5 + 2^4 + (2^3 - 2^2) + (2^1 - 2^0)
 \end{aligned}$$

FIG. 2

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STATE	INPUT	NEXT	ACTION
H0	$i < 0$	DONE	SUBTRACT
H0	$b_i = 0$	H1	DOUBLE, ADD
H0	$b_i = 1$	H0	DOUBLE, ADD
H1	$i < 1$	DONE	
H1	$b_i = 0$	H1	DOUBLE, SUBTRACT
H1	$b_i = 1$	H0	DOUBLE, SUBTRACT

FIG. 3

```

BEGIN :
    i := N          ; START FROM MSB          L 1
    Q := 0          ; INITIALIZE ACCUMULATOR  L 2
    H := 0          ; INITIALIZE STATE        L 3

LOOP :          ; FOR ALL BITS
    Q := Q + Q      ; DOUBLE ACCUMULATOR      L 4

    IF H = 0        ; IF H STATE IS SET        L 5
        Q := Q + P  ; ADD BASE POINT TO ACCUMULATOR L 6
        GOTO ENDL00P ;                      L 7
    ELSE            ; ELSE
        Q := Q + (-P) ; SUBTRACT BASE POINT      L 8
        GOTO ENDL00P ;                      L 9

ENDL00P :
    H := b[i]       ; SET H STATE TO VALUE OF b[i] L 10
    i := i - 1      ; PROCESS NEXT BIT          L 11
    IF i > 0         ; IF BIT EXISTS            L 12
        GOTO LOOP   ; CONTINUE AT TOP OF LOOP    L 13

    IF H = 0        ; IF EXISTING FROM H = 0 STATE L 14
        Q := Q + (-P) ; CORRECT RESULT BY FINAL SUBTRACT L 15
    END             ;                      L 16

```

FIG. 4

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```

BEGIN :
    i := N          ; START FROM MSB          LL 1
    Q := 0          ; INITIALIZE ACCUMULATOR  LL 2

H0 :          ; STATE ENTRY POINT
    Q := Q + Q      ; DOUBLE ACCUMULATOR      LL 3
    Q := Q + P      ; ADD BASE POINT TO ACCUMULATOR LL 4
    GOTO ENDL00P    ; BRANCH TO END OF LOOP TESTS LL 5

H1 :          ; STATE ENTRY POINT
    Q := Q + Q      ; DOUBLE ACCUMULATOR      LL 6
    Q := Q + (-P)   ; SUBTRACT BASE POINT FROM ACCUMULATOR LL 7
    GOTO ENDL00P    ; BRANCH TO END OF LOOP TESTS LL 8

ENDL00P :      ; END OF LOOP TESTS
    IF b[i]=1      ; IF CURRENT BIT IS SET      LL 9
        GOTO NEXT H0 ; FOLLOW H0 PATH          LL 10
    ; ELSE FALL INTO H1 PATH

NEXT H1 :      ; H1 PATH
    i := i - 1     ; PROCESS NEXT BIT          LL 11
    IF i > 0        ; IF BIT EXISTS            LL 12
        GOTO H1     ; EXECUTE H1 STATE          LL 13
    Q := Q + (-P)   ; ELSE CORRECT RESULT AND END LL 14
    END            LL 15

NEXT H0 :      ; H0 PATH
    i := i - 1     ; PROCESS NEXT BIT          LL 16
    IF i > 0        ; IF BIT EXISTS            LL 17
        GOTO H0     ; EXECUTE H0 STATE          LL 18
    END            ; ELSE END                  LL 19

```

FIG. 5

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```
BEGIN :  
    i : = N  
    Q : = 1  
  
H0 :  
    Q : = Q · Q ( $Q^2$ )  
    Q : = Q · M  
    GOTO ENDLOOP  
  
H1 :  
    Q : = Q · Q  
    Q : = Q / M ( $Q \cdot M^{-1}$ )  
  
ENDLOOP :  
    IF b[i] = 1 GOTO ENDLOOP  
  
NEXT H1 :  
    i = i - 1  
    IF i > 0  
        GOTO H1  
    Q = Q / M  
    END  
  
NEXT H0 :  
    i = i - 1  
    IF i > 0  
        GOTO H0  
    END
```

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FIG. 6

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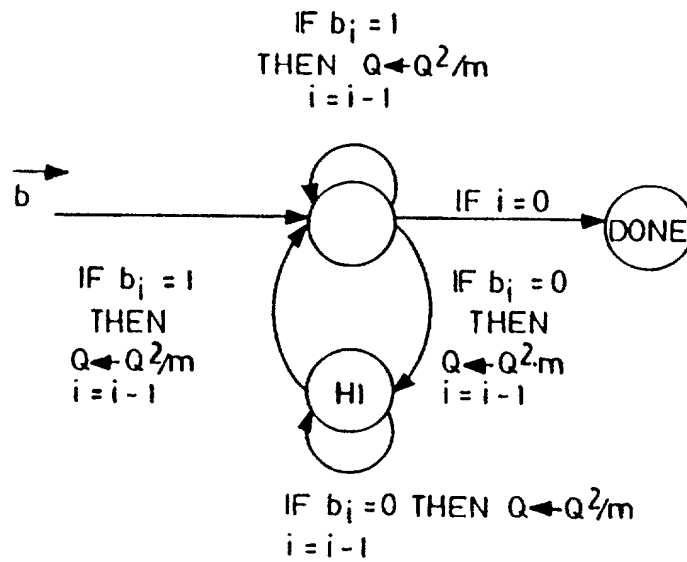


FIG. 7

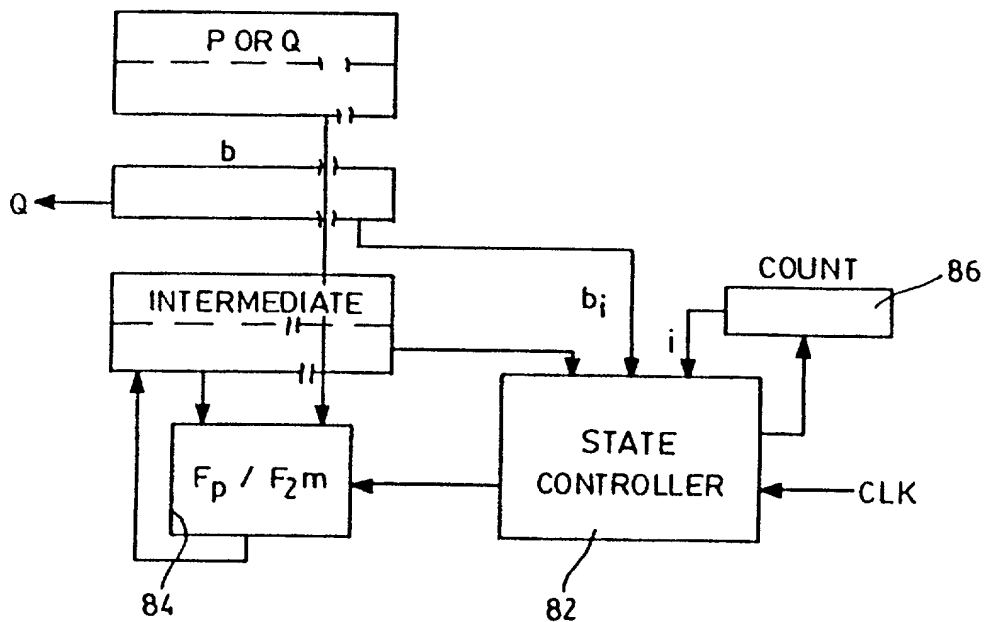


FIG. 8